

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S10 4	0	(field adj program\$5 adj array) and (singulat\$3 dic\$3 cut\$3) and (customer near2 order)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/27 10:20
S10 6	0	(field near2 gate5 near2 array) and (singulat\$3 dic\$3 cut\$3) and (customer near2 order)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/27 10:23
S10 5	0	(field adj gate5 adj array) and (singulat\$3 dic\$3 cut\$3) and (customer near2 order)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/27 10:23
S10 8	102	(field near2 gate5 near2 array "FPGA" "PLD") and (singulat\$3 dic\$3 cut\$3) and (customer near2 order)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/27 10:41
S10 7	73	(field near2 gate5 near2 array "FPGA") and (singulat\$3 dic\$3 cut\$3) and (customer near2 order)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/27 10:41
S10 9	2	(FPPA) and (singulat\$3 dic\$3 cut\$3) and (customer near2 order)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/27 10:42

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S11 0	31	(US-20020072135-\$ or US-20020079921-\$ or US-20010044918-\$ or US-20040060032-\$ or US-20020125910-\$ or US-20060081971-\$ or US-20050062495-\$ or US-20040161878-\$ or US-20040119098-\$ or US-20040061147-\$ or US-20020043988-\$ or US-20030206036-\$ or US-20060176075-\$).did. or (US-6962829-\$ or US-5581742-\$ or US-5581738-\$ or US-6996758-\$ or US-6600495-\$ or US-6910201-\$ or US-7032190-\$ or US-7007250-\$ or US-6983439-\$ or US-6769107-\$ or US-7020860-\$ or US-6953956-\$ or US-6362651-\$ or US-6222212-\$ or US-6157213-\$ or US-7105871-\$ or US-5822256-\$ or US-7228521-\$). did.	US-PGPUB; USPAT	OR	ON	2007/12/27 10:43
S11 5	4433	S114 and (wafer substrate)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/27 13:01
S11 3	40637	(field near2 program\$5 near2 array)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/27 13:01
S11 2	2	("20010038297").PN.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/12/27 13:01
S11 6	255	S115 and (dic\$3 singulat\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/27 13:02

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S11 4	30367	S113 and (customer near3 order order)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/27 13:11
S11 7	1720	(ASIC) and (customer near3 order order) and (wafer substrate) with (dic\$3 scrib\$3 street\$3 singulat\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/27 13:12
S11 9	55	(rapidchip)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/27 13:33
S11 8	126	S117 and S113	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/27 13:33
S12 0	0	(rapidchip) and (singulat\$3 dic\$3° cut\$3) and (customer near2 order)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/27 16:15
S12 2	226087	(program\$5) near5 (arry cell unit)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/27 16:17
S12 1	129	(array adj unit cell) with (core and processor) and (singulat\$5 dic\$3 street\$3 scrib\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/27 16:17

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S12 3	42	S121 and S122	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/27 16:31
S12 5	339756	(customer near3 order customer)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/27 16:32
S11 1	26	S110 and (customer near3 order order)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/27 16:32
S12 6	41	S125 and S124	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/27 16:37
S12 4	246	(FPGA field near3 gate near2 array "PAU") and (wafer substrate) with (dic\$3 singulat\$3 scrib\$3 street\$3) and (silicon "Si")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/27 16:38
S12 7	823	(program\$5) and (wafer substrate) with (dic\$3 singulat\$3 scrib\$3 street\$3) and (silicon "Si") and (core) and (processor computer)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/27 16:39
S12 8	386	S127 and (@ad<"20040126")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/27 17:41

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S12 9	2995810	(cut\$3 or scrib\$3 singulat\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/27 17:42
S13 2	203	S131 and (chip with rout\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/27 17:43
S13 1	2332	S129 and S130	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/27 17:43
S13 0	16898	(connect\$4 or interconnect\$4) with (FPGA program\$5 near3 array program\$3 near3 cell)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/27 17:43
S10 1	405	S100 and (@ad<"20040126")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/27 17:43
S13 4	174	(platform near5 array) with (wafer substrate) and (silicon "Si")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/28 13:01
S13 3	184	S132 and (@ad<"20040126")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/28 13:56

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Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L19	1017	(platform near5 array FPGA PLD prgramm\$5 near2 logic (programm\$3 near4 (array cell))) with (core and processor)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/28 13:54
L20	388	(field adj programm\$5 near2 gate) with (core and processor)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/28 14:20
L21	109	20 and (@ad<"20040126")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/28 14:21
L22	6	("5059899" "5696404" "5767565" "6150840").PN. OR ("6362651").URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2007/12/28 14:17
L23	2	("20070089082").PN.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/12/28 14:17
L24	909	(field adj programm\$5 near2 gate "FPGA") with (core and processor)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/28 17:09
L25	800	24 not 21	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/28 14:21

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L26	315	25 and (@ad<"20040126")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/28 14:46
L27	2	"20020072135"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/28 14:41
L28	2	("6801564").PN.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/12/28 14:41
L29	60	(IC intergrated adj circuit LSI) with (silicon Si) and (FPGA (field near3 (array cell))) and (core and processor) and (dic\$3 singulat\$3 cut\$3 scrib\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/28 14:52
L30	4	(platform adj array) with (wafer) with (silicon "Si")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/28 14:52
L31	4771	(program\$3 array) with (wafer) with (silicon "Si")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/28 14:53
L32	13	(program\$3 near4 (array tile)) with (wafer) with (silicon "Si")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/12/28 14:53

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L33	2	("20010011345").PN.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/12/28 17:10
L34	2	("20010038297").PN.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/12/28 17:30
L35	2	("6222212").PN.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/12/28 17:35
L36	2	("6396129").PN.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/12/28 17:35